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- 1. ``VLSI Digital Signal Processing Syustems'', Keshab K. Parhi, Wiley Eastern*
- 2. ``Digital Signal Processing for Multimedia Systems'', Keshab K. Parhi and Takao Nishitani, Marcel Dekker.*
- 3. ``Pipelined Lattice and Wave Digital Recursive Filters'', J. G. Chung and Keshab K. Parhi, Kluwer.*

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[*Big List of VLSI, Signal Processing, etc. Conferences ...*](#)

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Research Interests: VLSI design for digital signal processing, biochemical signal processing, 5G communication system design. Dr. Xiao-Ping Zhang, Professor Dept. of Electrical and Computer Engineering Ryerson University 350 Victoria Street, Toronto, Ontario CANADA, M5B 2K3 Phone: +1-416-979-5000 ext. 6686, Fax: +1-416-979-5280

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The design space for the implementation of digital signal processing tasks of communications systems in VLSI (very large scale integration) is very large, which makes the implementation a challenging task. The design space is explored, and implementation problems are pointed out.

[Very Large Scale Integration - Wikipedia](#)

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He has served as technical program cochair of the 1995 IEEE VLSI Signal Processing workshop and the 1996 ASAP conference, and as the general chair of the 2002 IEEE Workshop on Signal Processing Systems. He was a distinguished lecturer for the IEEE Circuits and Systems society during 1996-1998, and is serving a second term currently (2019-20).

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Design and implementation of digital signal processing (DSP) and communications systems in VLSI:

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The objective here is to synthesize molecular reactions for a specified signal processing function. The products of these reactions can be used for protein monitoring and drug delivery. See my personal web page for more details. Publications: K.K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation, Wiley, NY 1999